

06/26/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>AG.4</u> Q.A. <u>Gu</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/891905	CONT/PRIOR F	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER Gelbrunn
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APPLICANTS

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TITLE

Methods of forming integrated circuits using masks to provide ion implantation shielding to portions of a substrate adjacent to an isolation region therein and integrated circuits formed using same

PTO-2040
12/99

ISSUING CLASSIFICATION								
ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION								

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ (Assistant Examiner) _____ (Date) _____ _____ (Primary Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
				ISSUE FEE Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	

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